

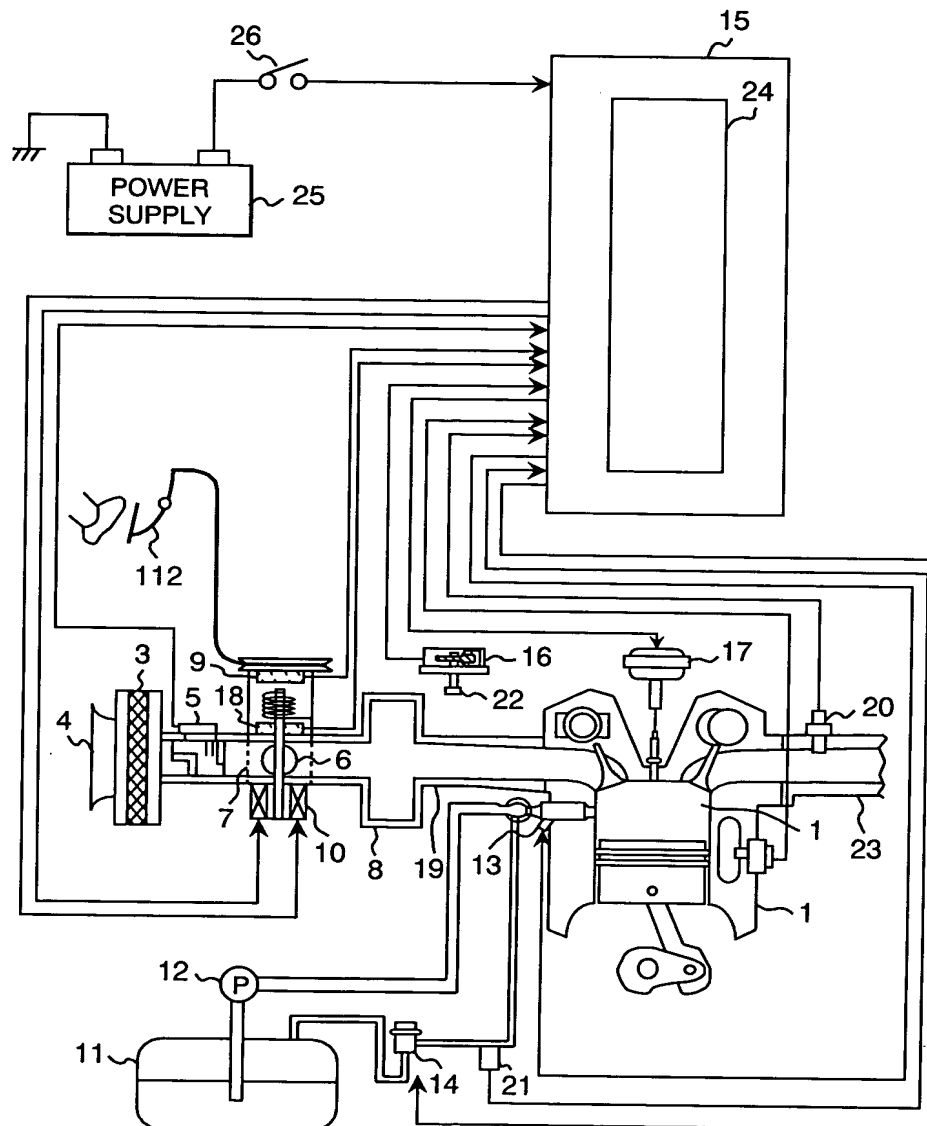
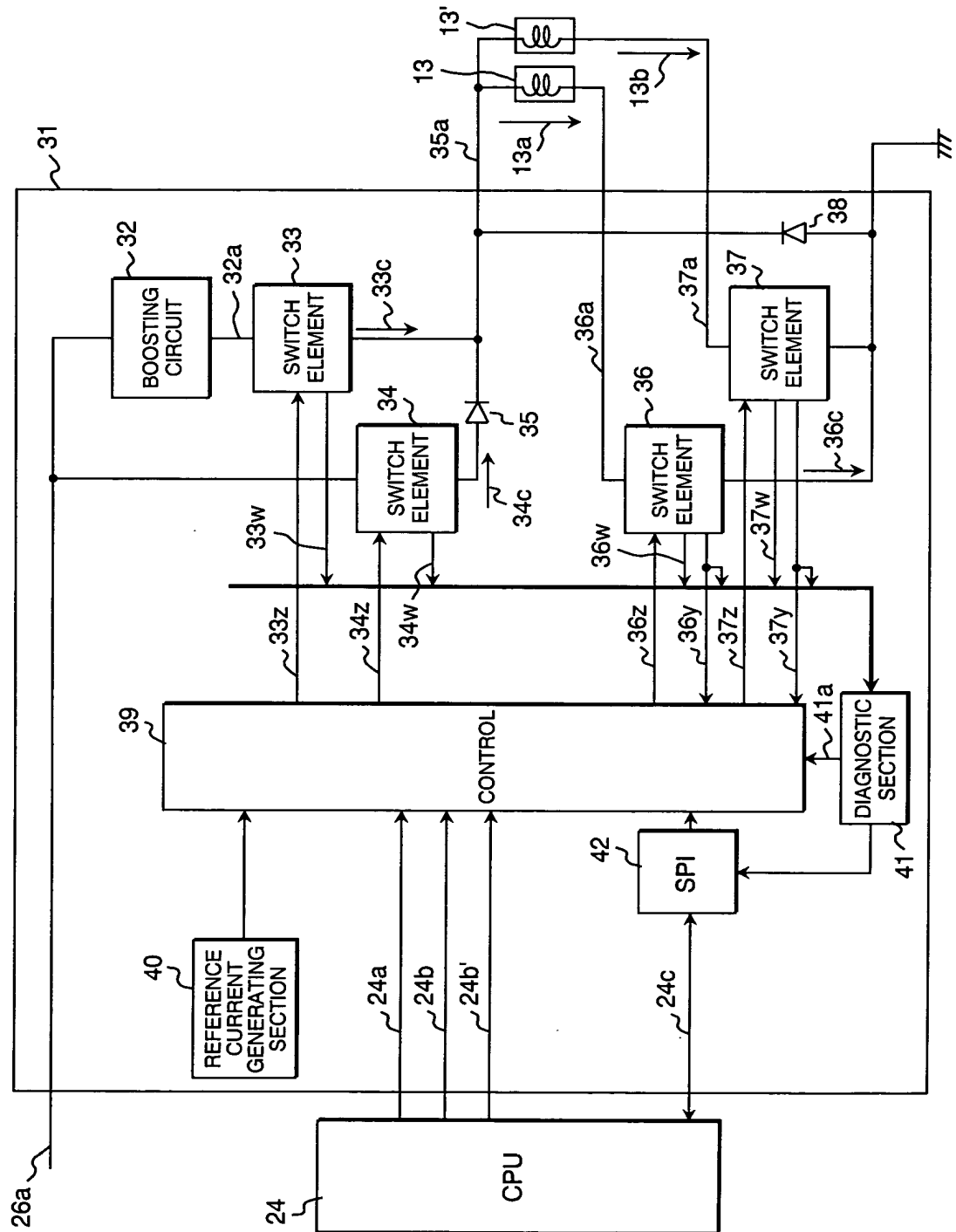


FIG. 2



3/15

FIG. 3

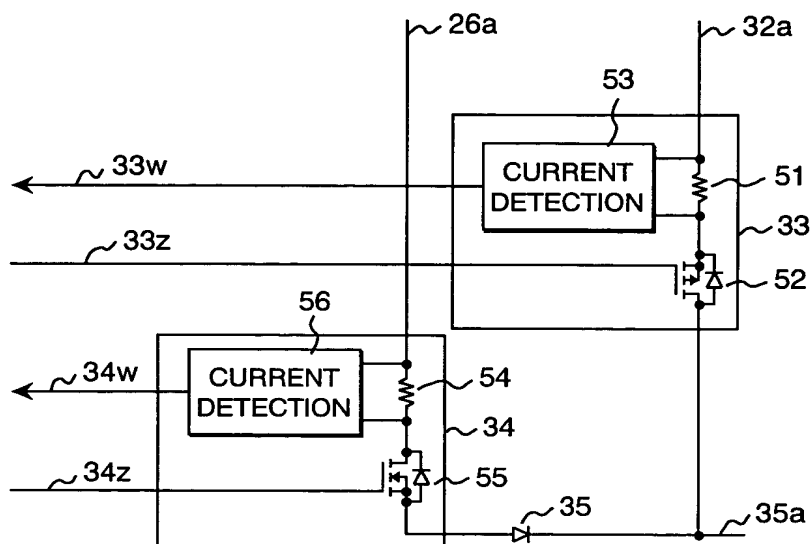
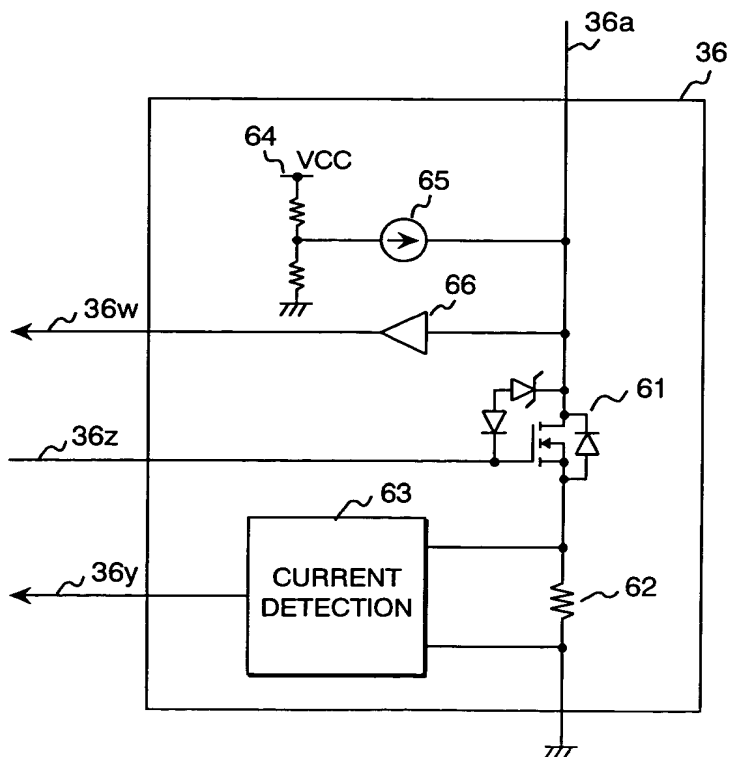


FIG. 4



4/15

FIG. 5

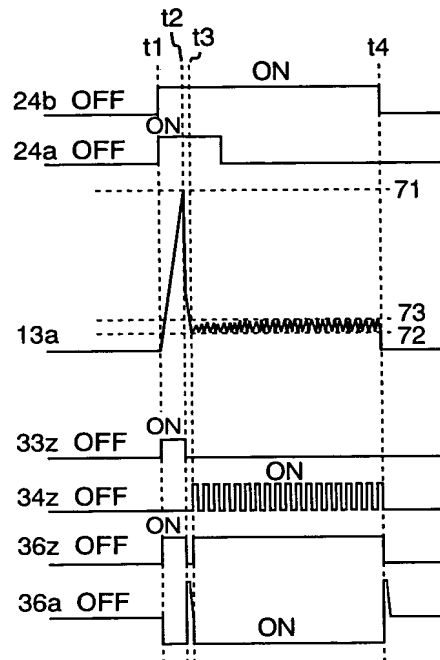
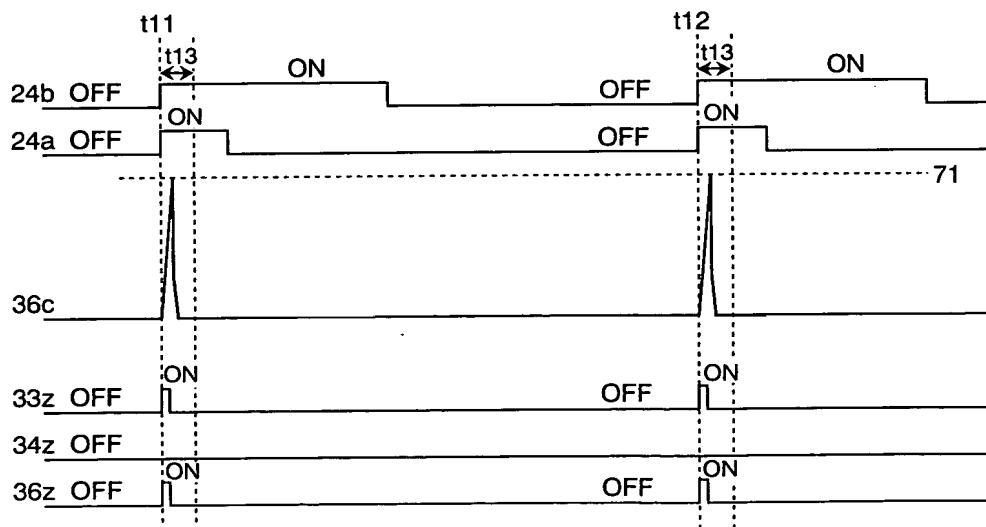
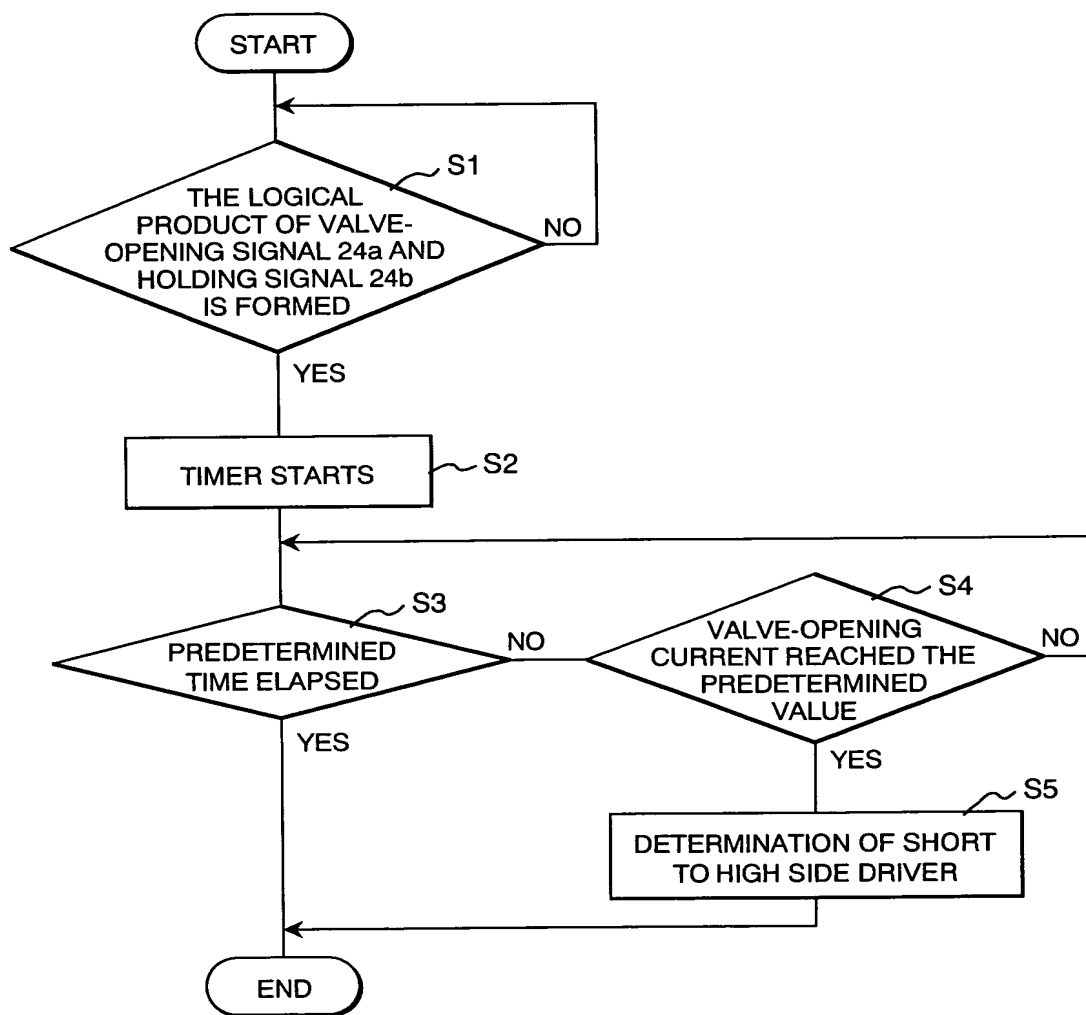


FIG. 6



5/15

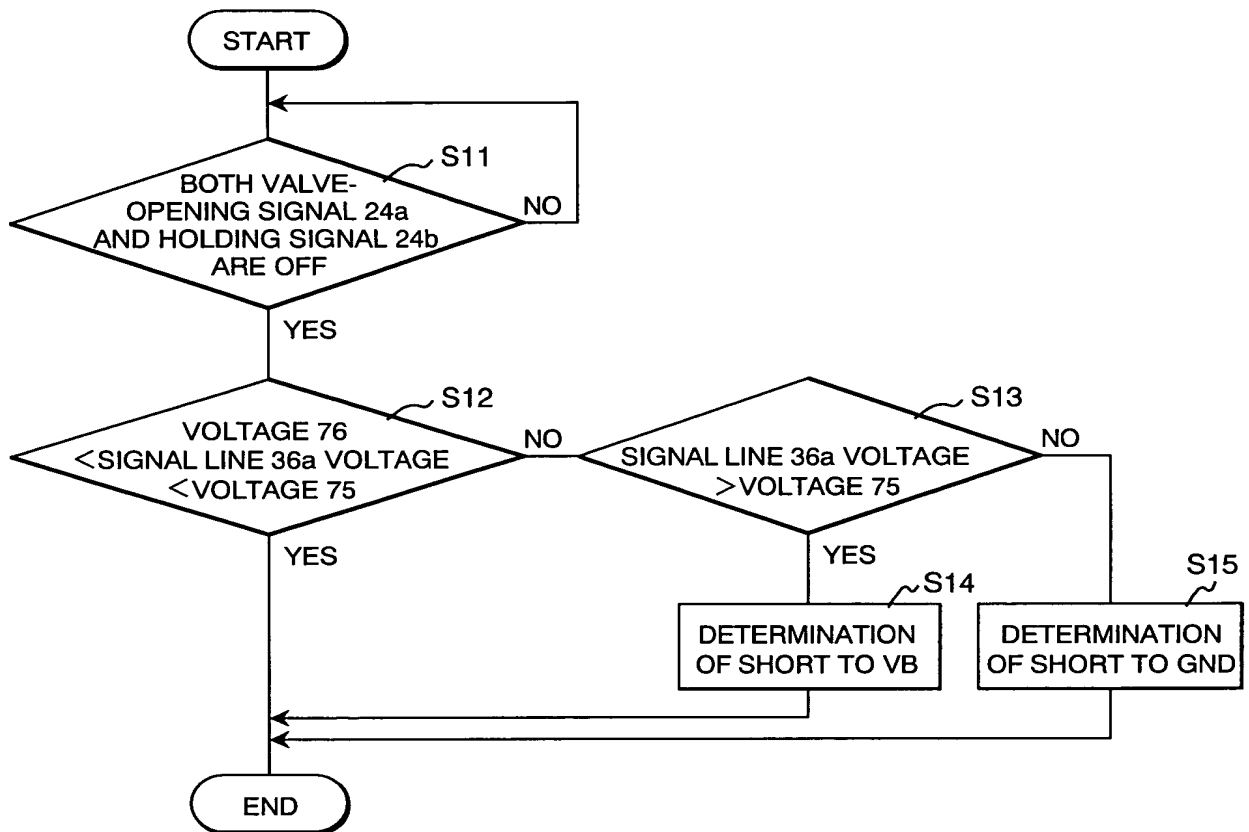
FIG. 7



The timing diagram shows the input and output waveforms for the 74VHC04 inverter. The input signals are 24b, 24a, 36c, 36a, 33z, 34z, and 36z. The output signals are 74, 71, 75, and 76. The diagram includes timing points t21, t23, t24, t25, t26, t27, t28, t29, and t30. Key events are labeled: "SHORT TO VB" (from t21 to t25), "SHORT TO GND" (from t26 to t27), and "SHORT TO VB" (from t29 to t30). The input signals 24b and 24a are shown as "OFF" and "ON" states. The output signals 74, 71, 75, and 76 are shown as "OFF" and "ON" states. The output signal 34z shows a "ON" state with a "SHORT TO GND" event.

7/15

FIG. 9



8/15

FIG. 10

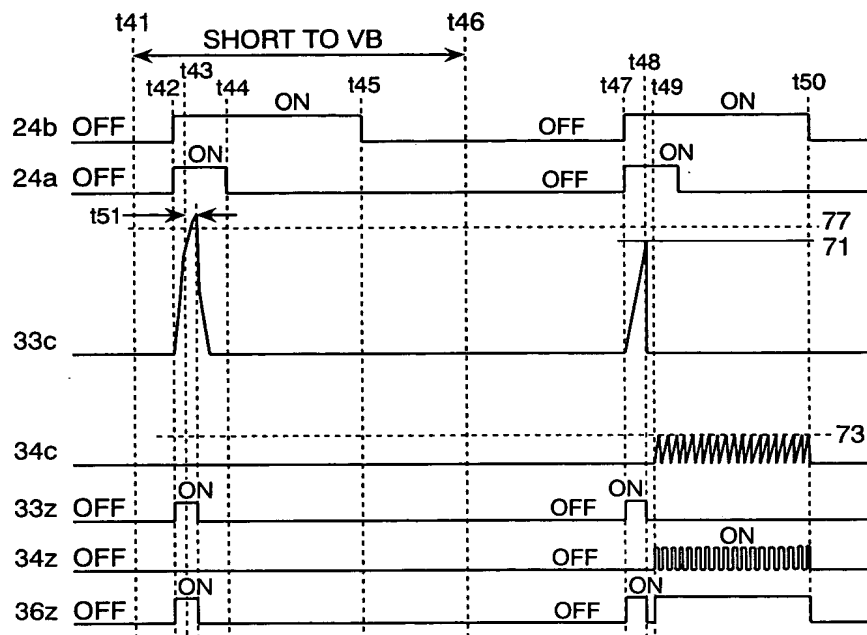
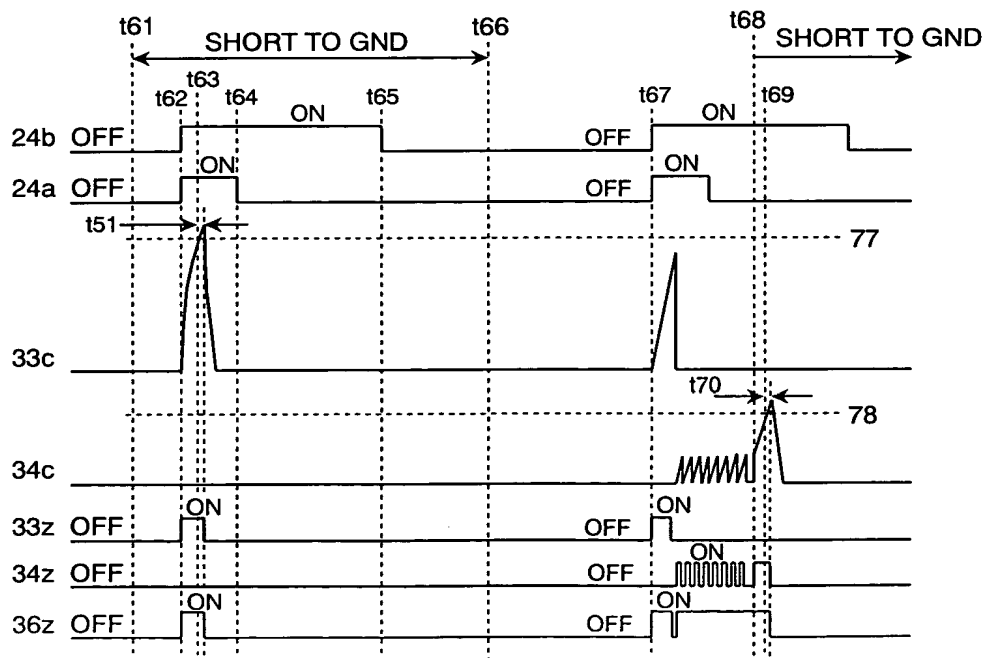
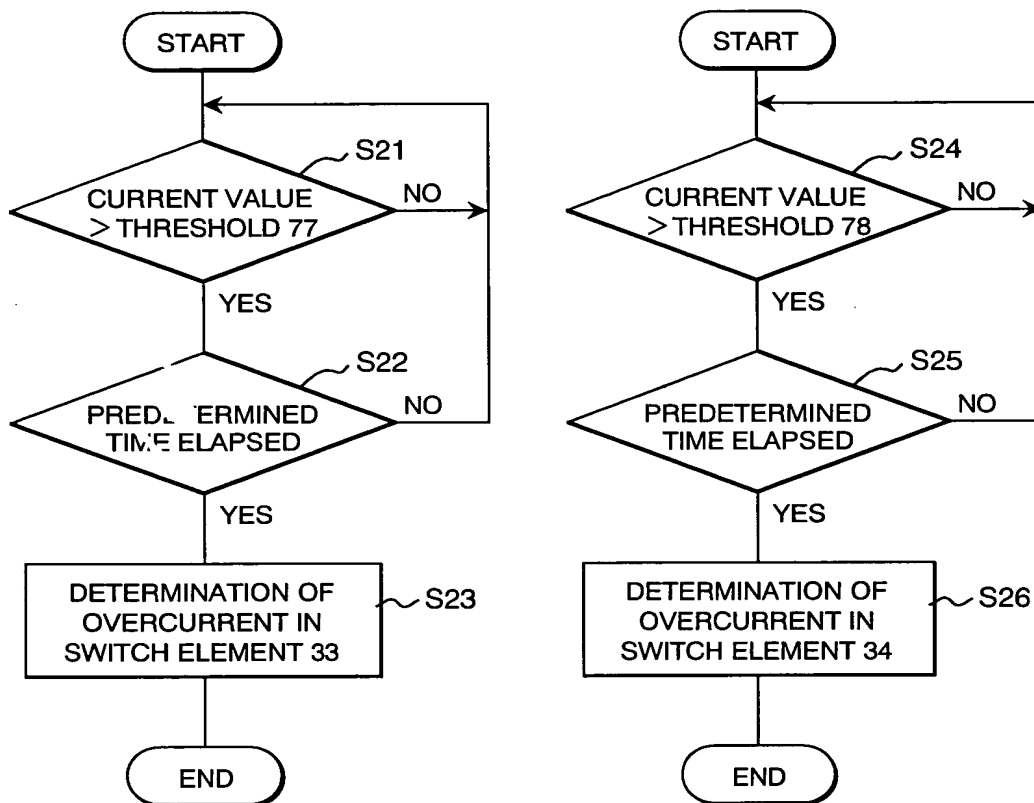


FIG. 11



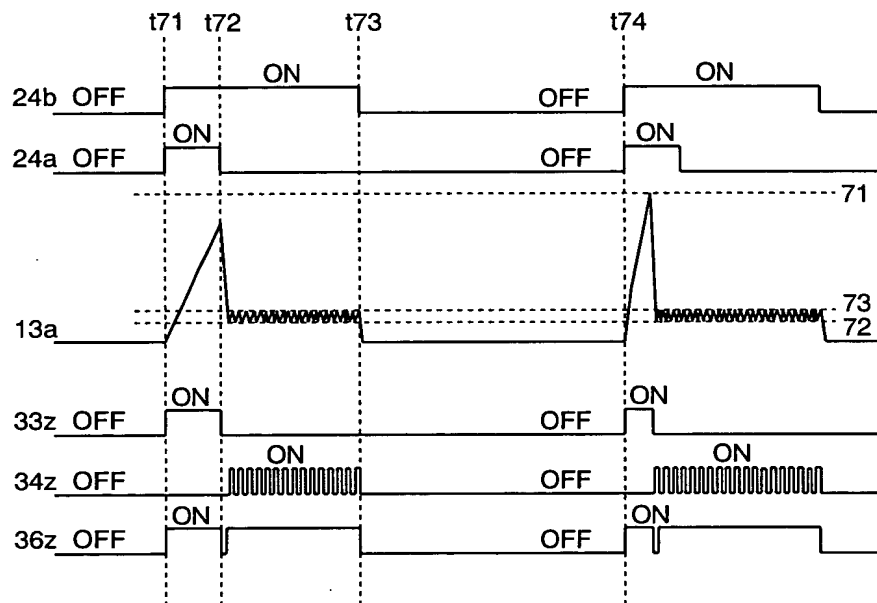
9/15

FIG. 12



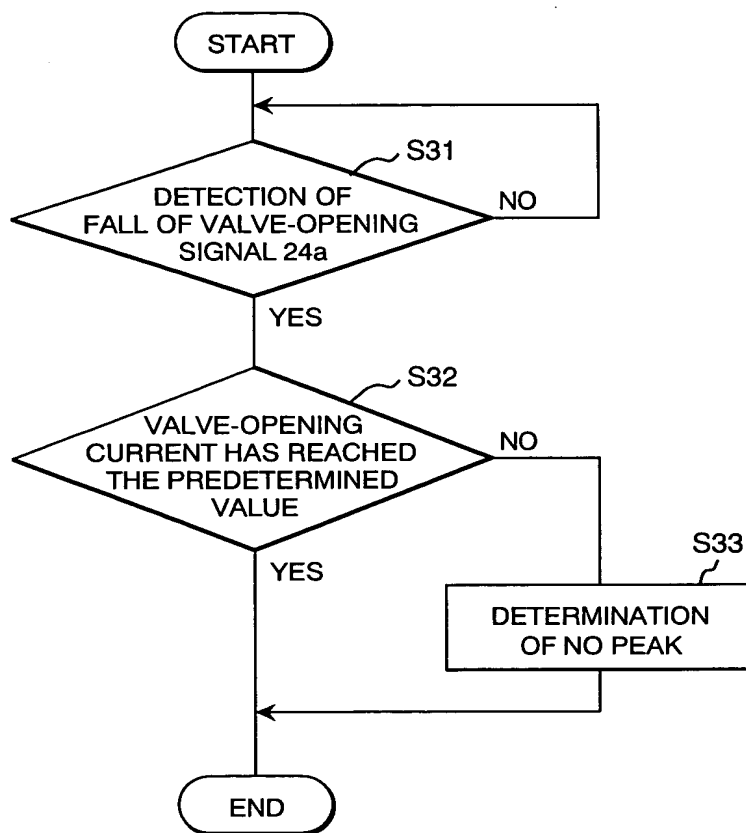
10/15

FIG. 13



11/15

FIG. 14



12/15

FIG. 15

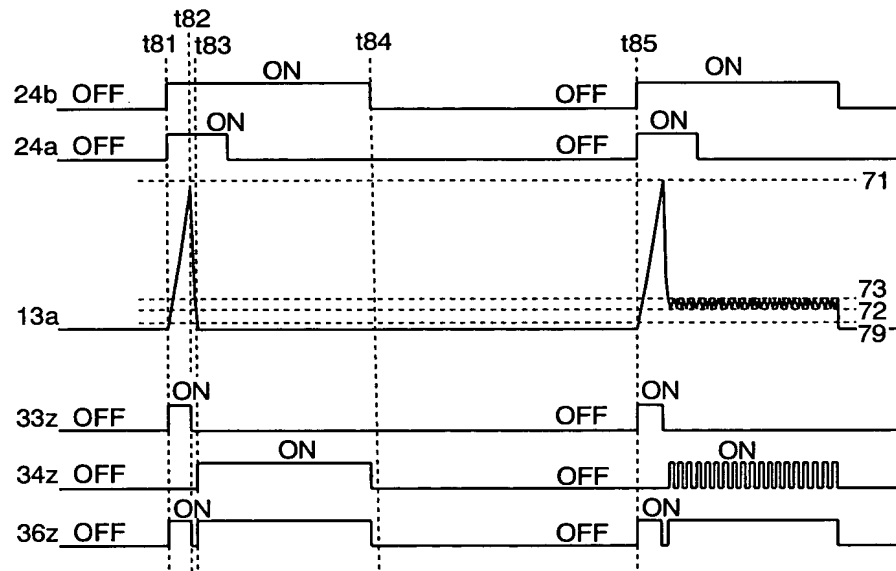
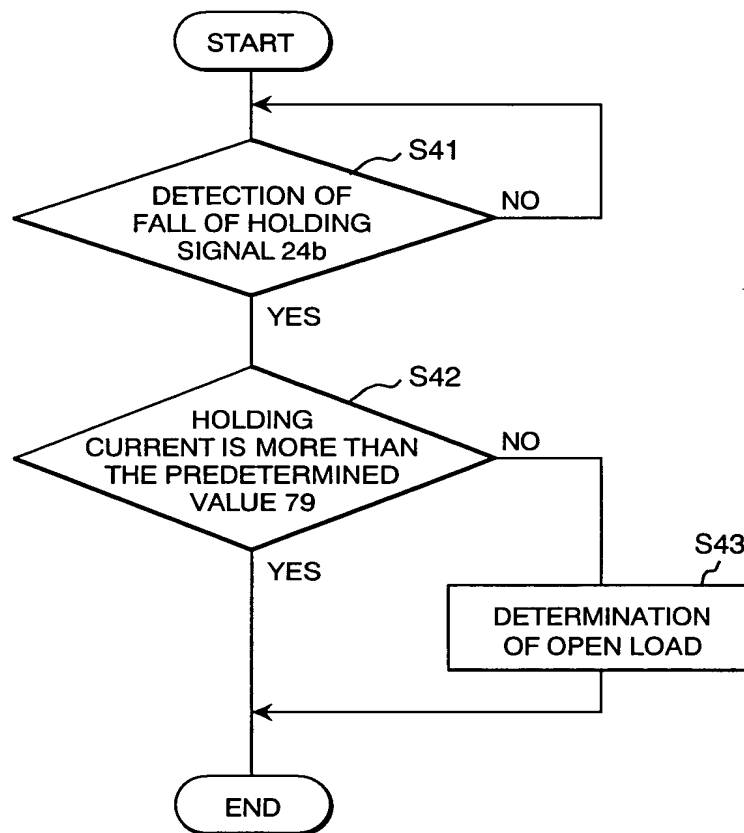
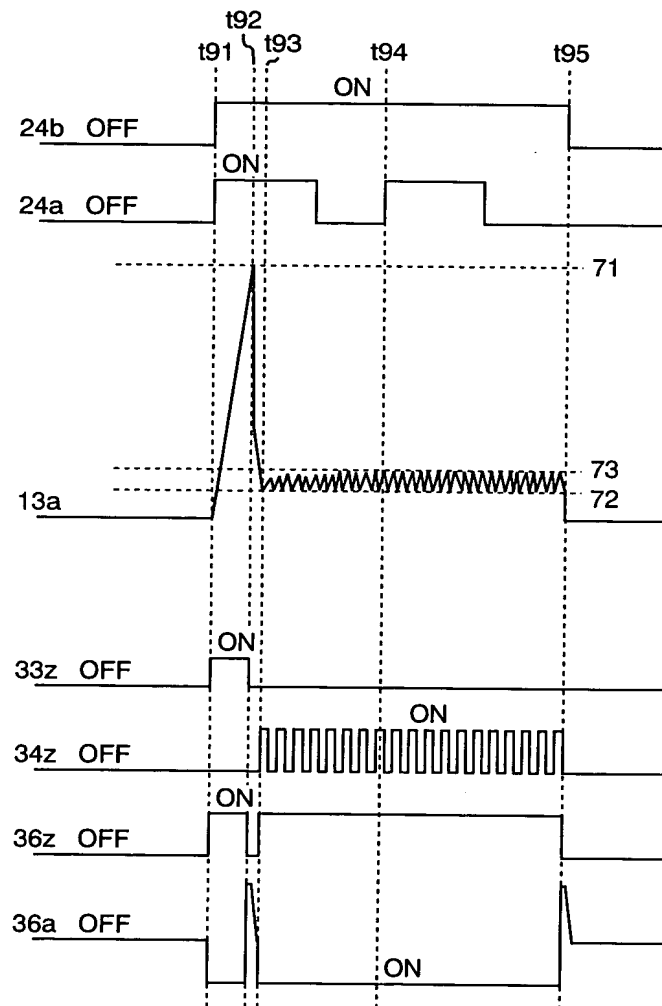


FIG. 16



14/15

FIG. 17



15/15

FIG. 18

